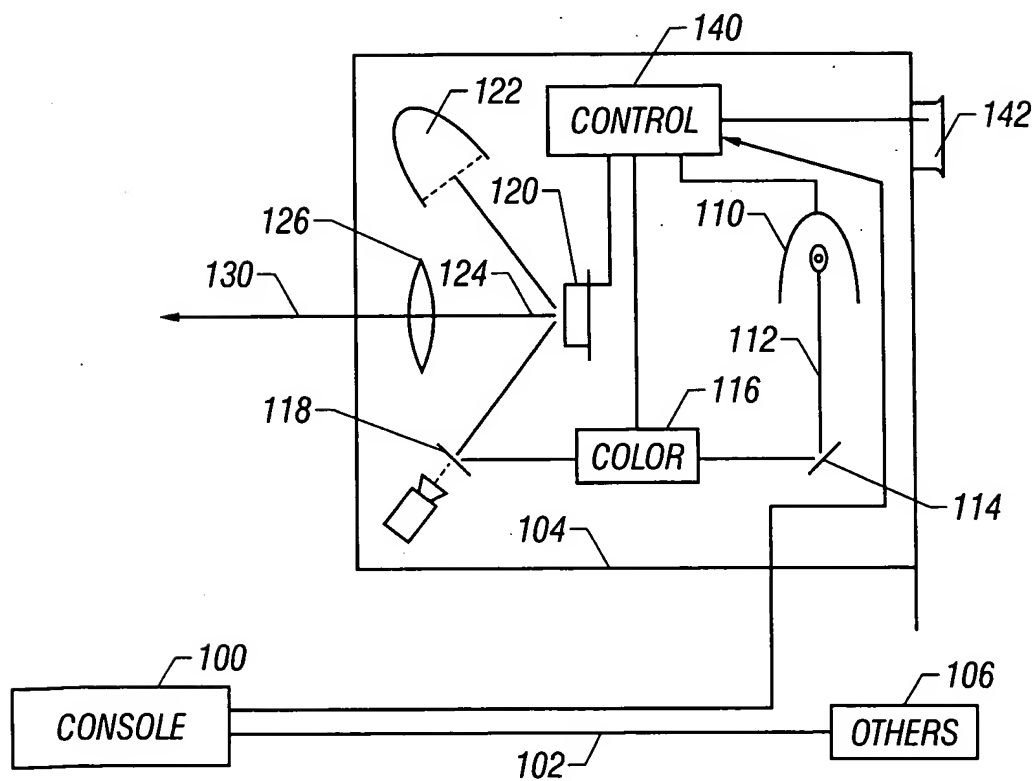


1/23**FIG. 1**

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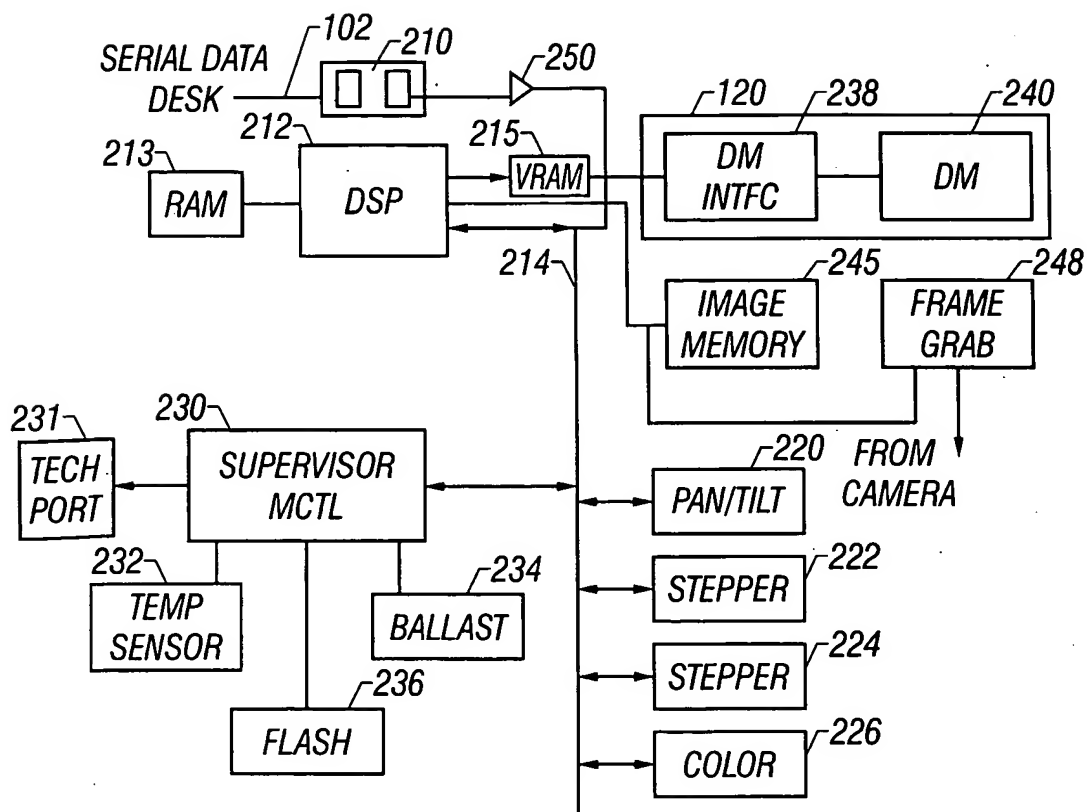


FIG. 2

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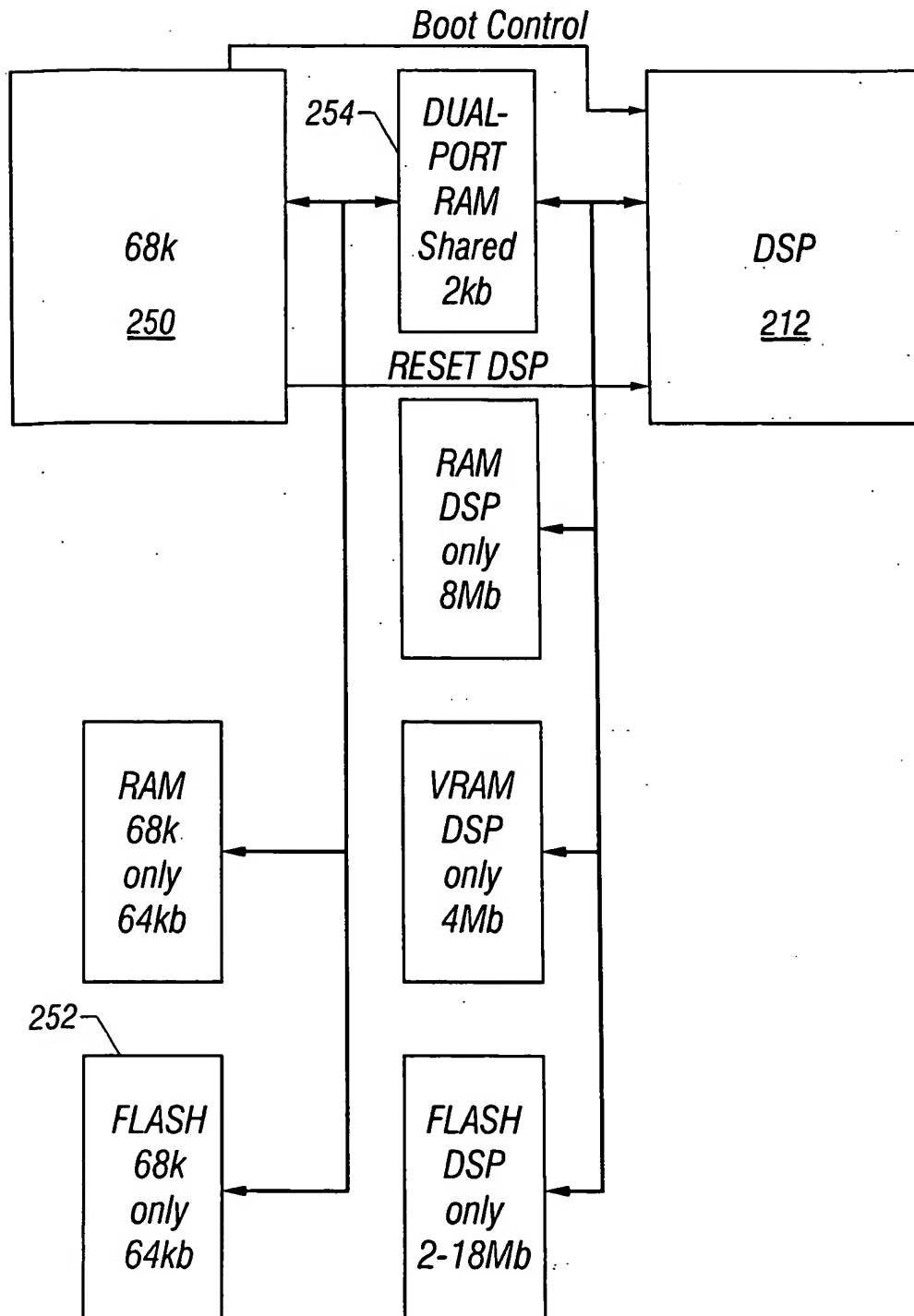


FIG. 2A

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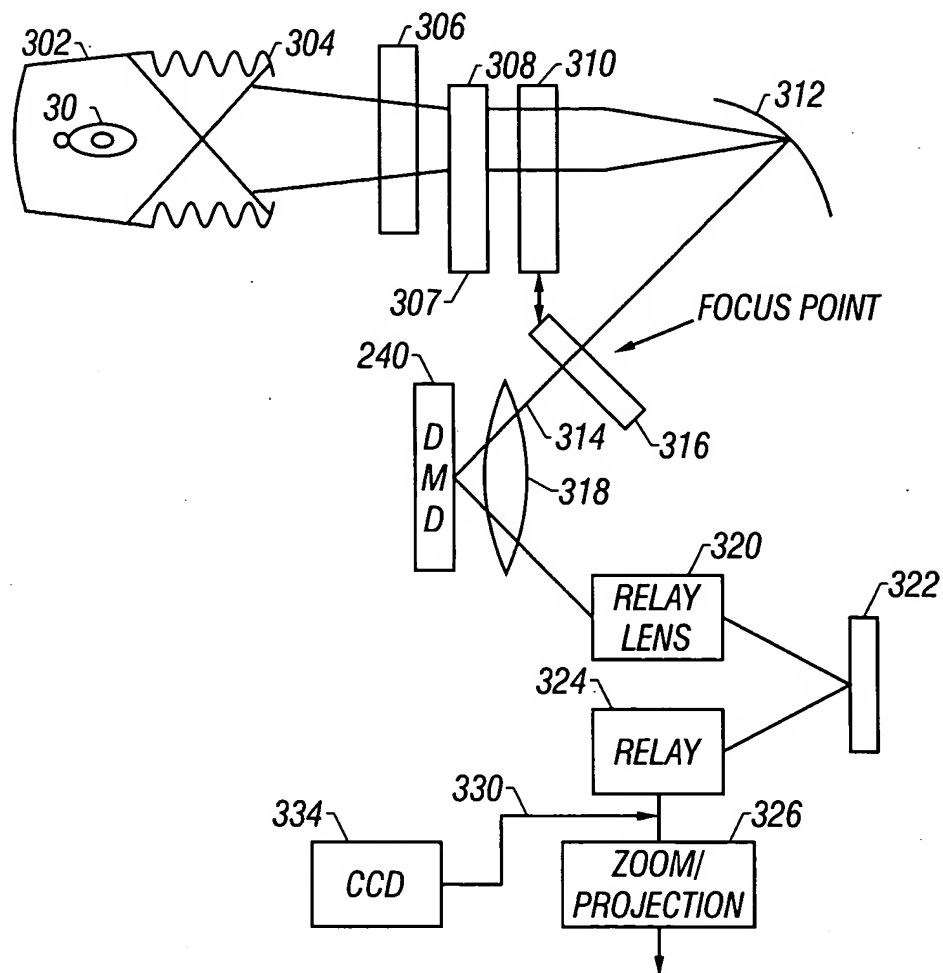


FIG. 3

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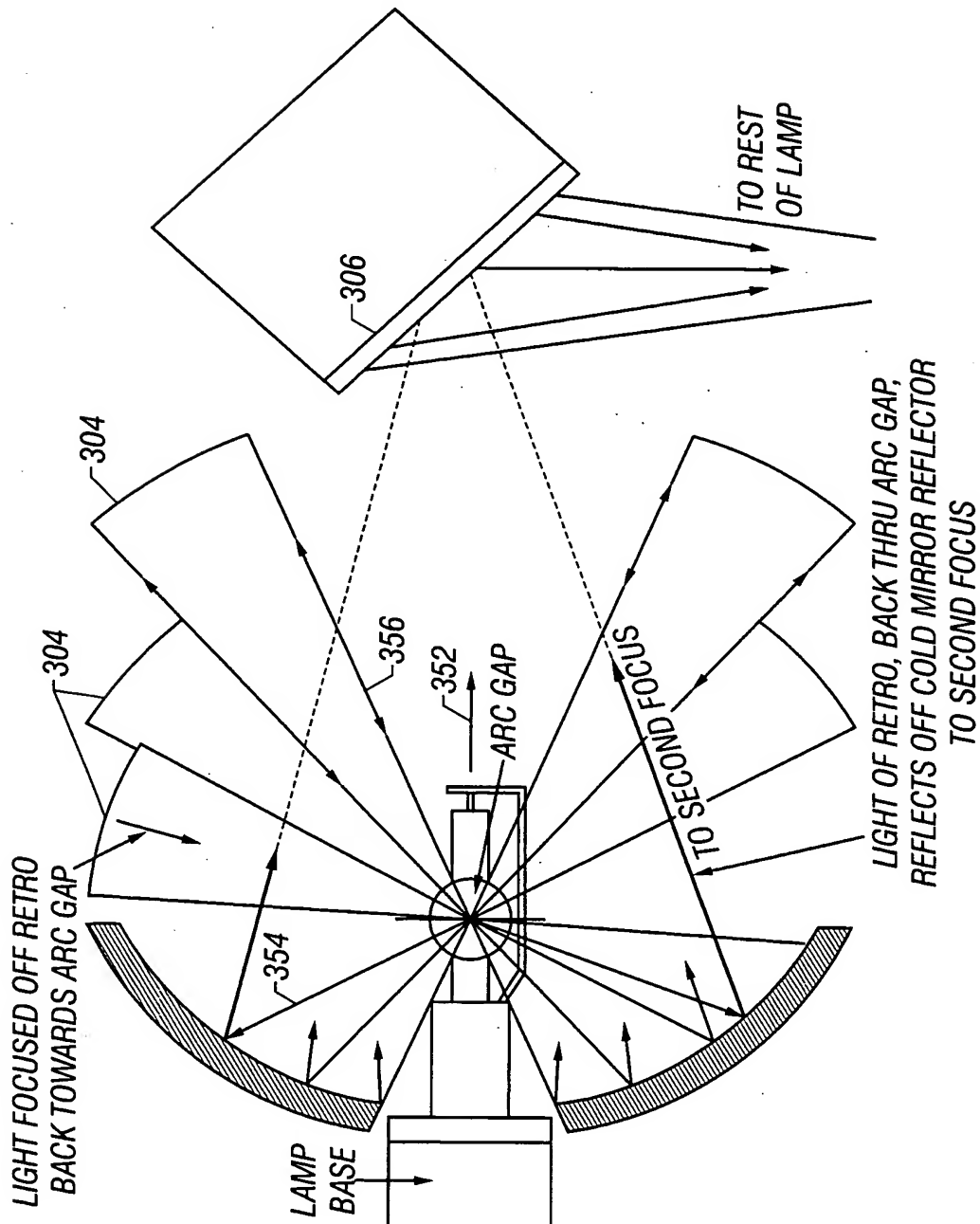


FIG. 3A

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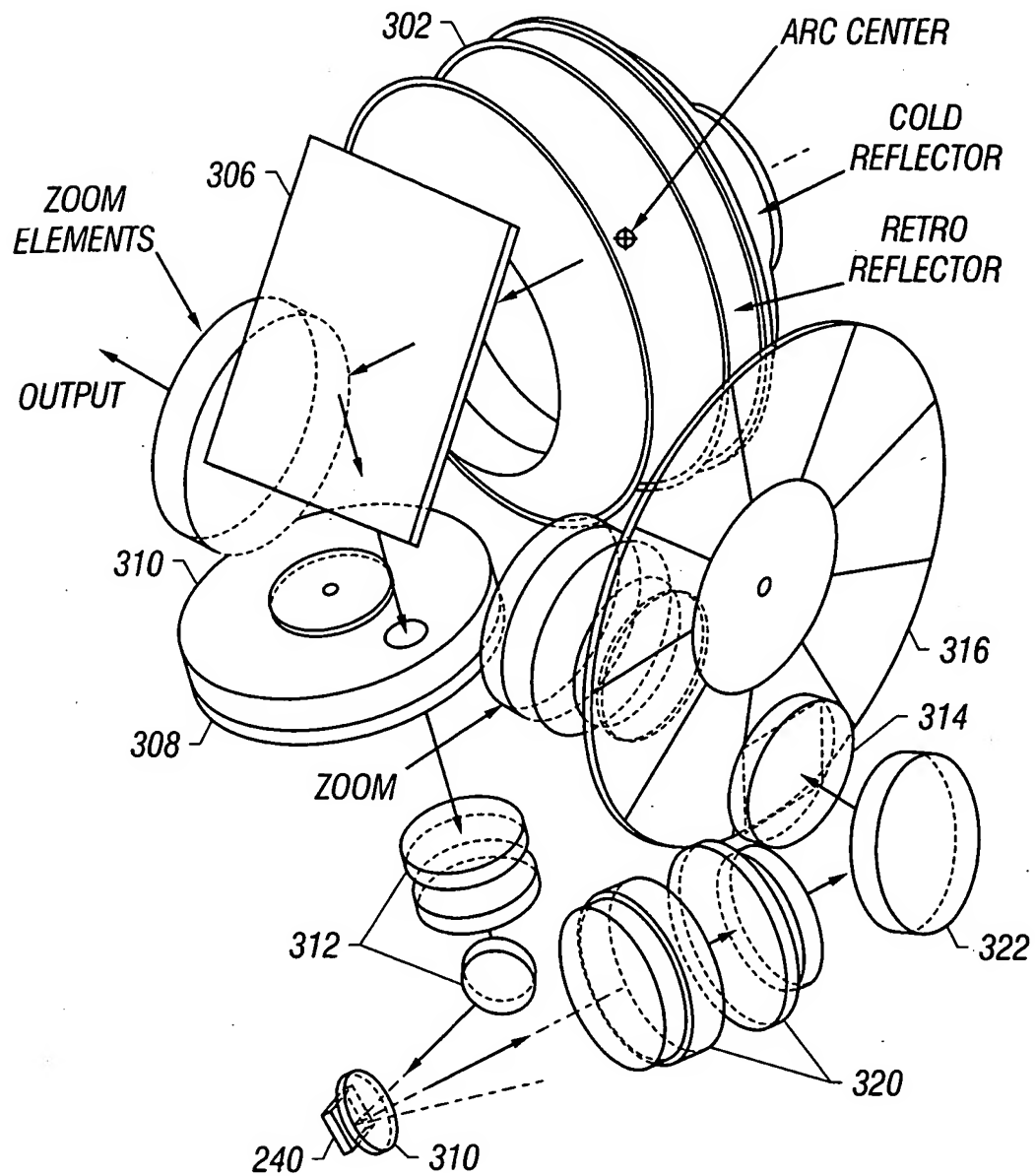


FIG. 3B

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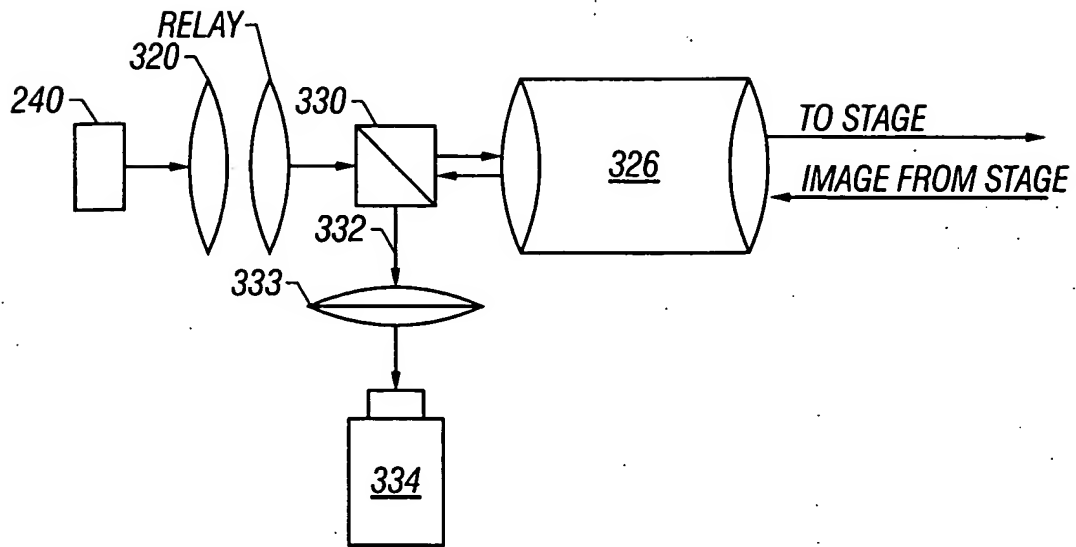


FIG. 3C

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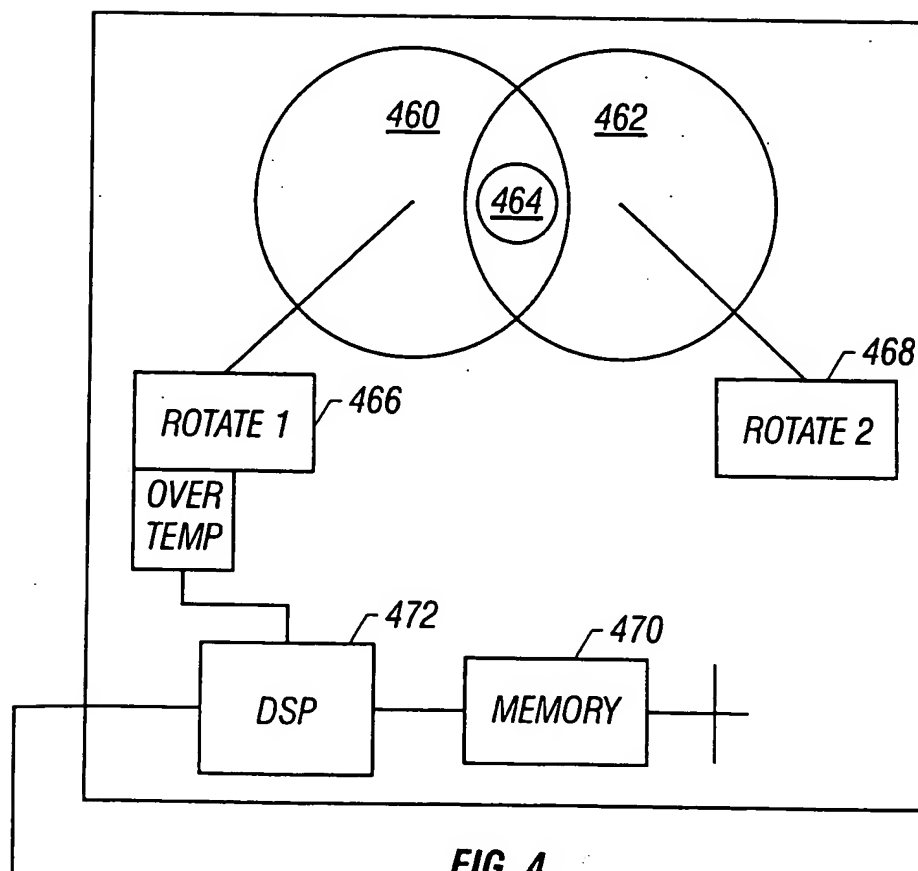
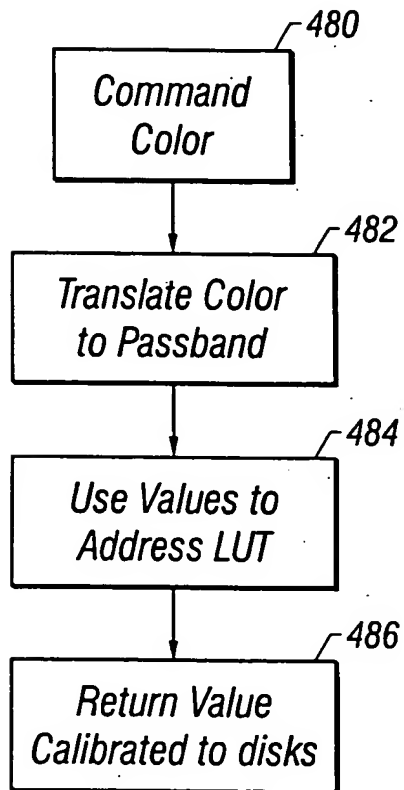


FIG. 4

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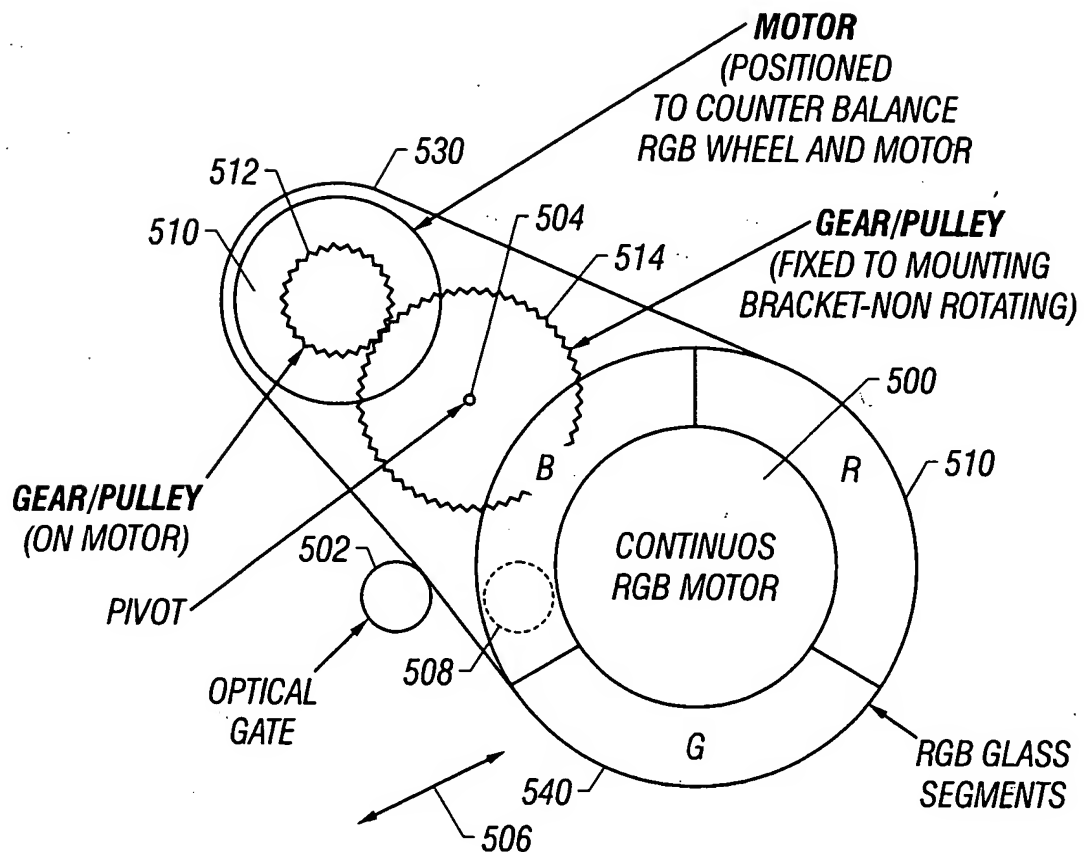
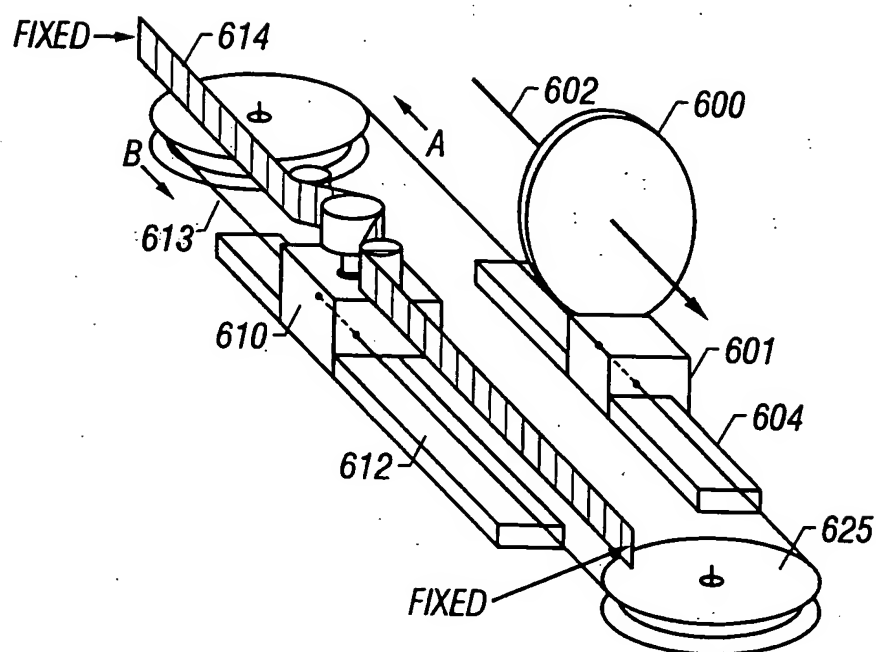
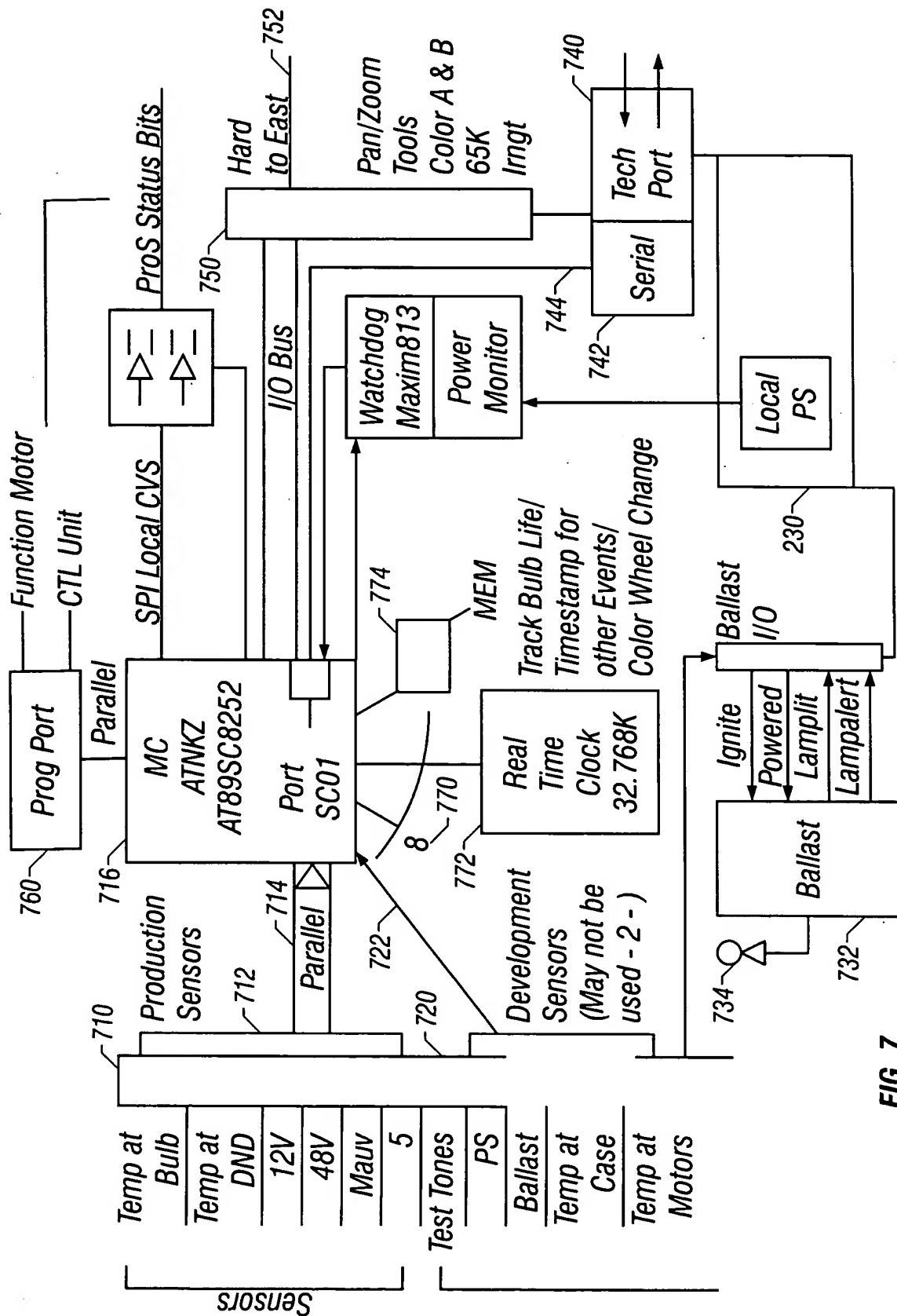


FIG. 5

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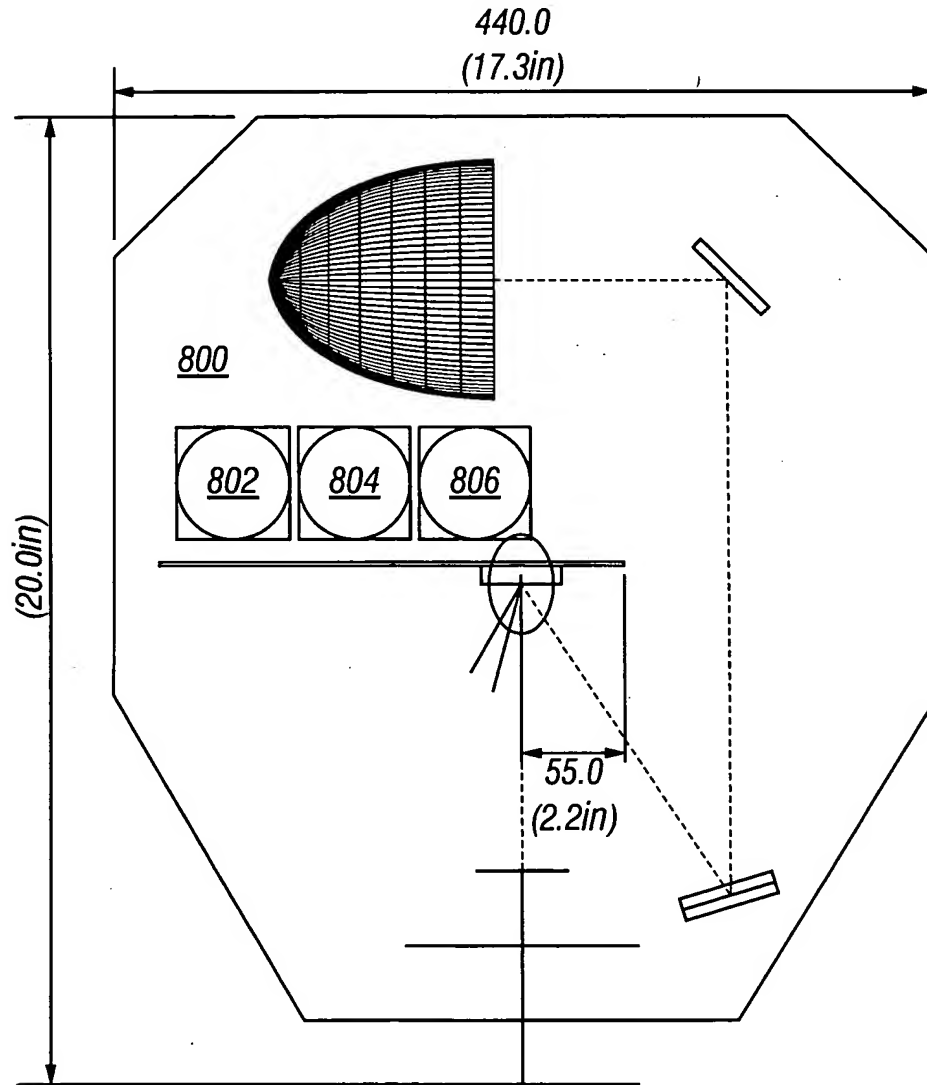
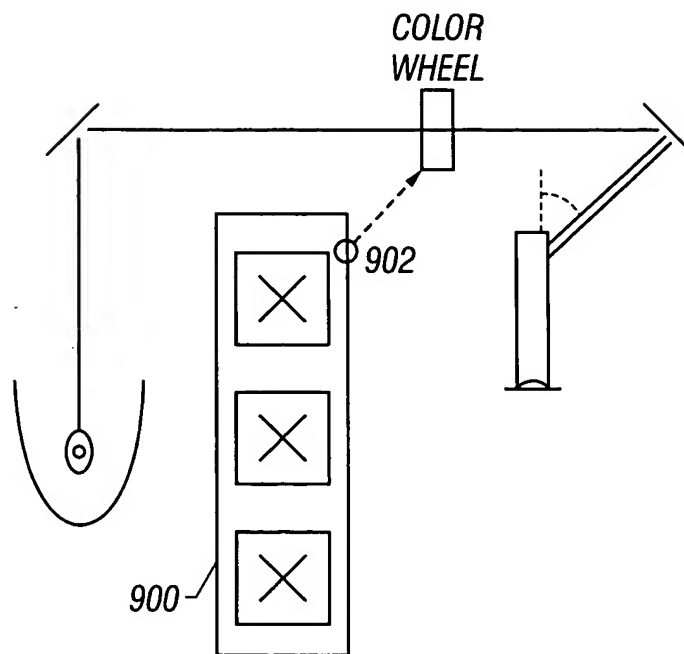


FIG. 8

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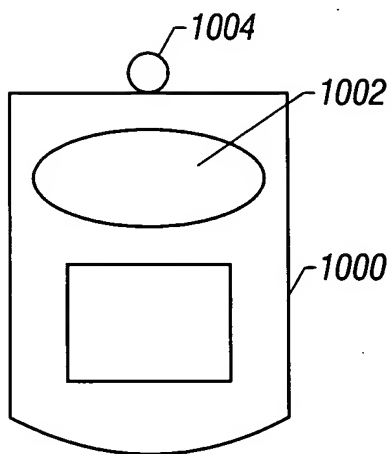


FIG. 10

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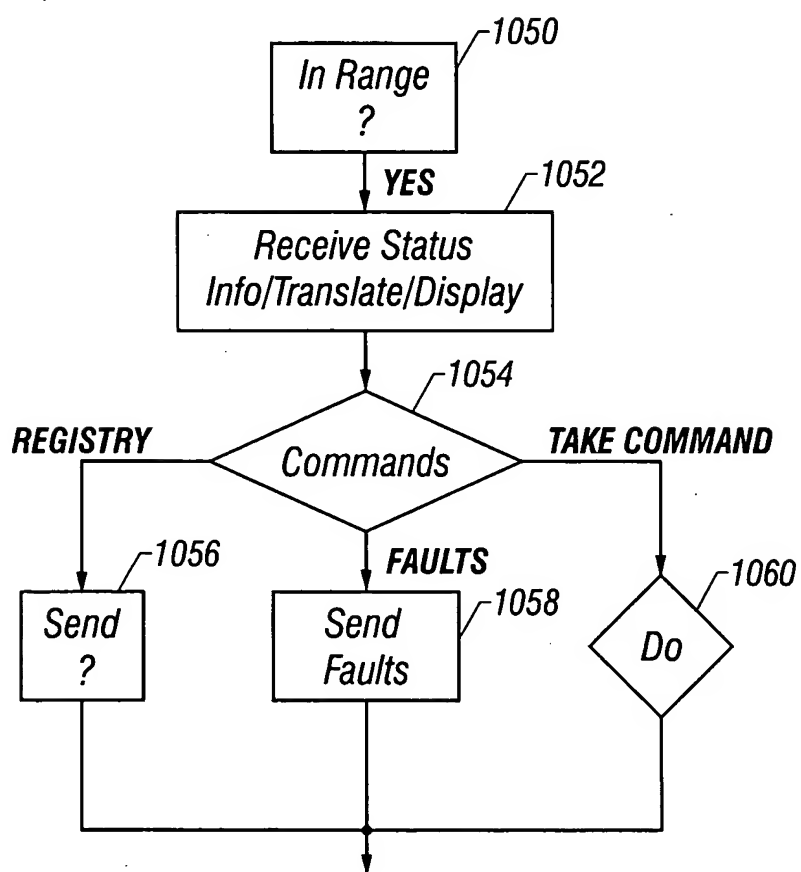


FIG. 10A

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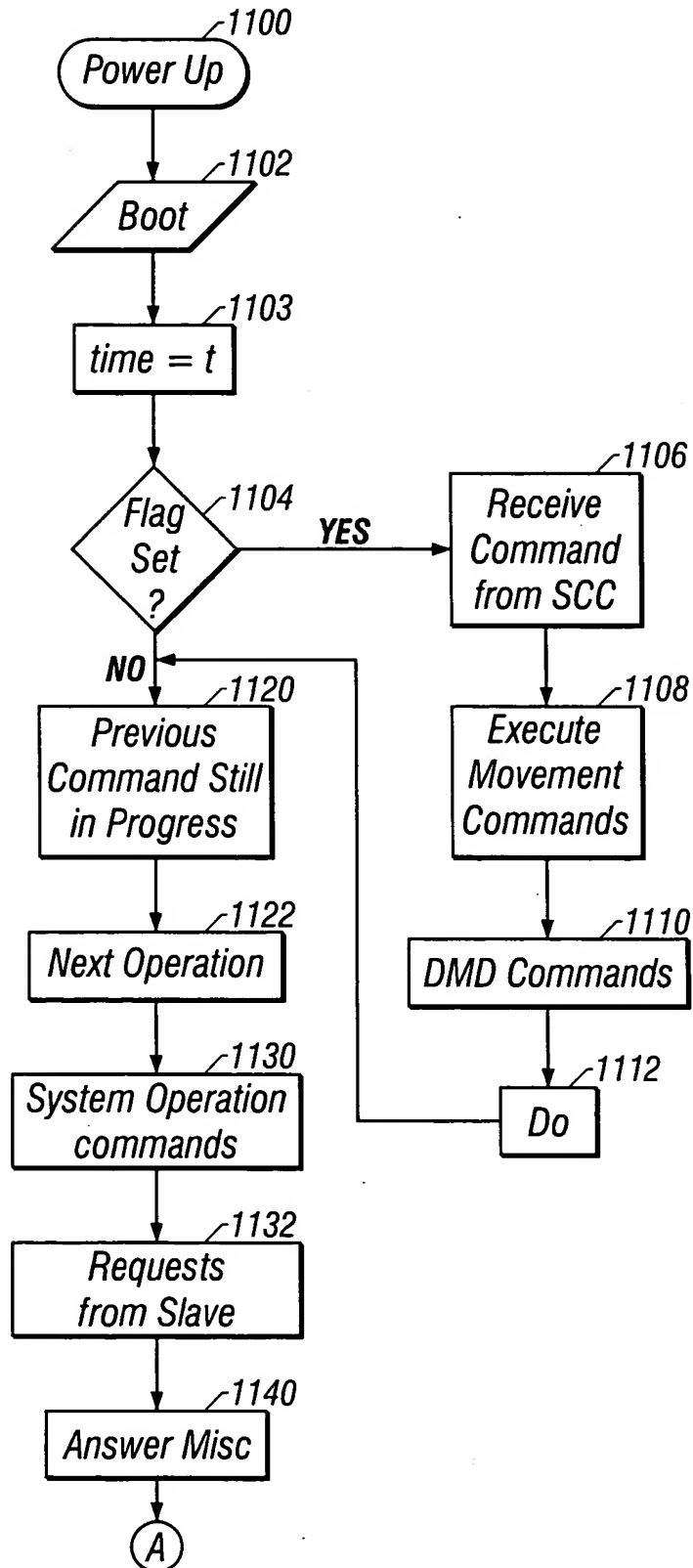
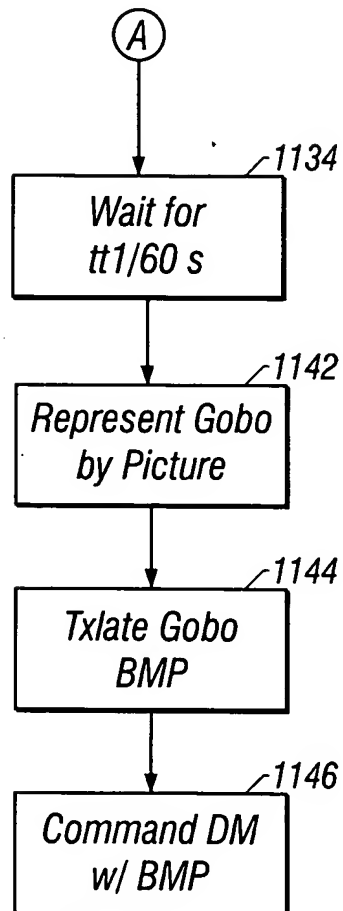


FIG. 11

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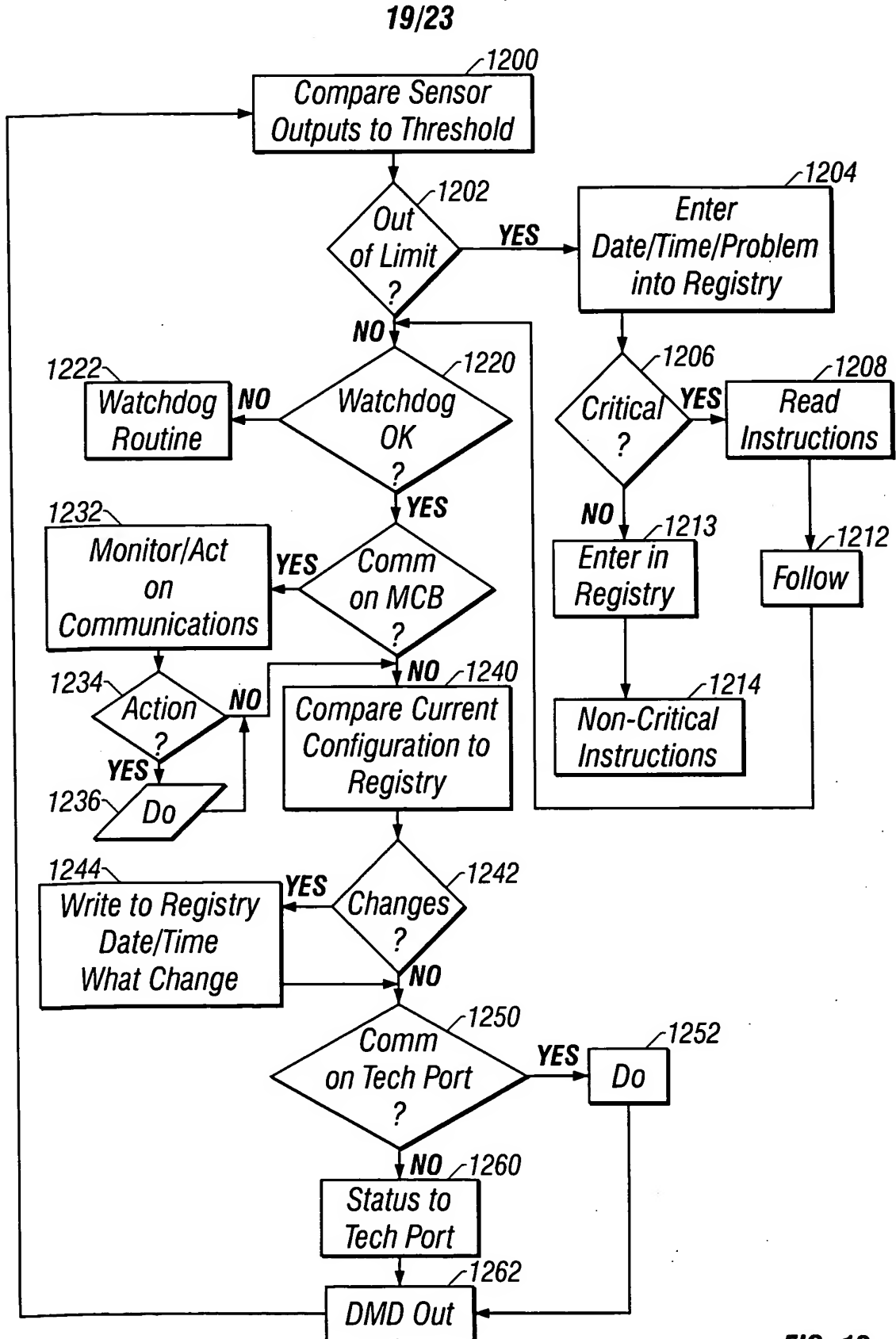


FIG. 12

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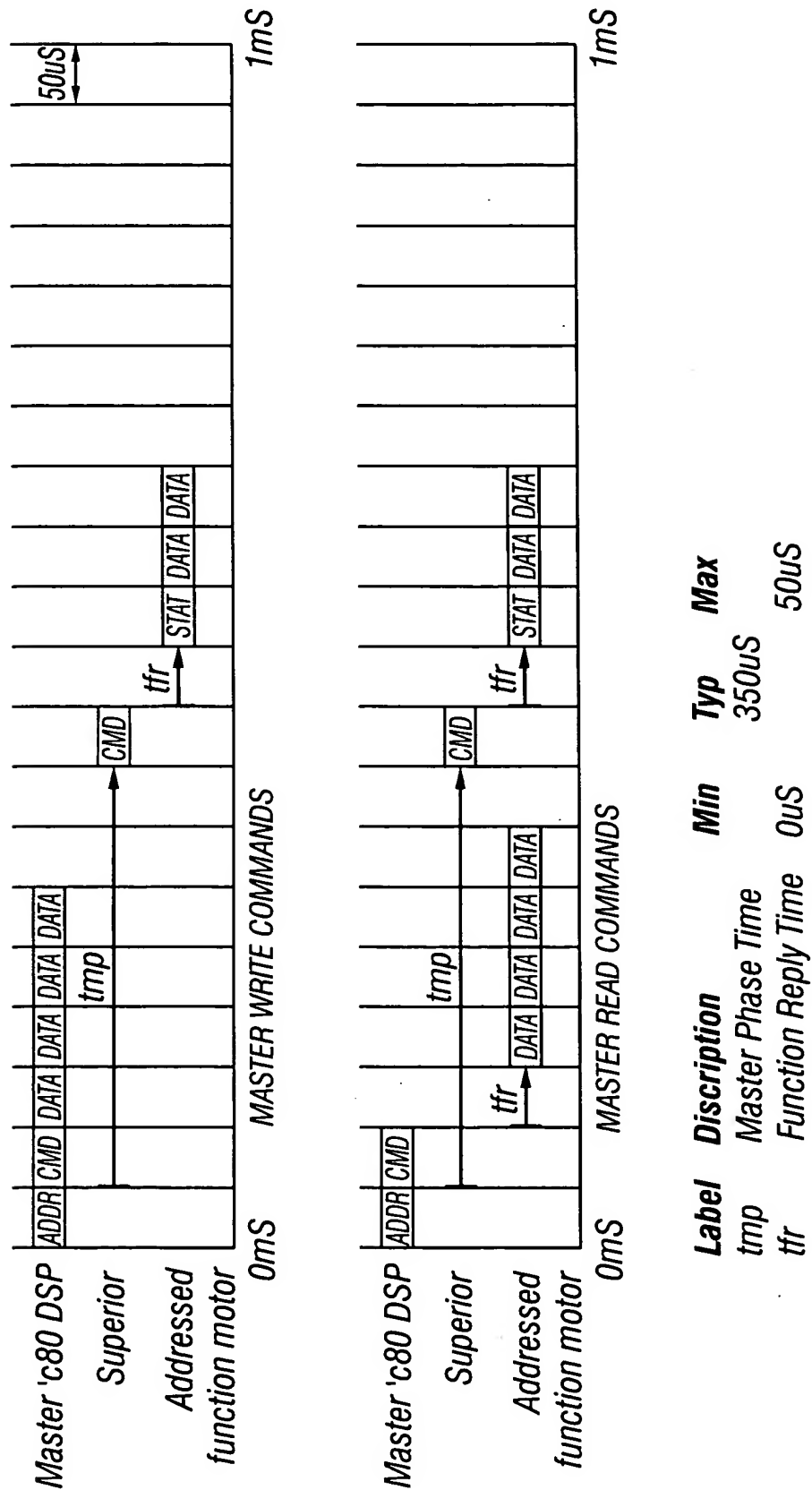


FIG. 13

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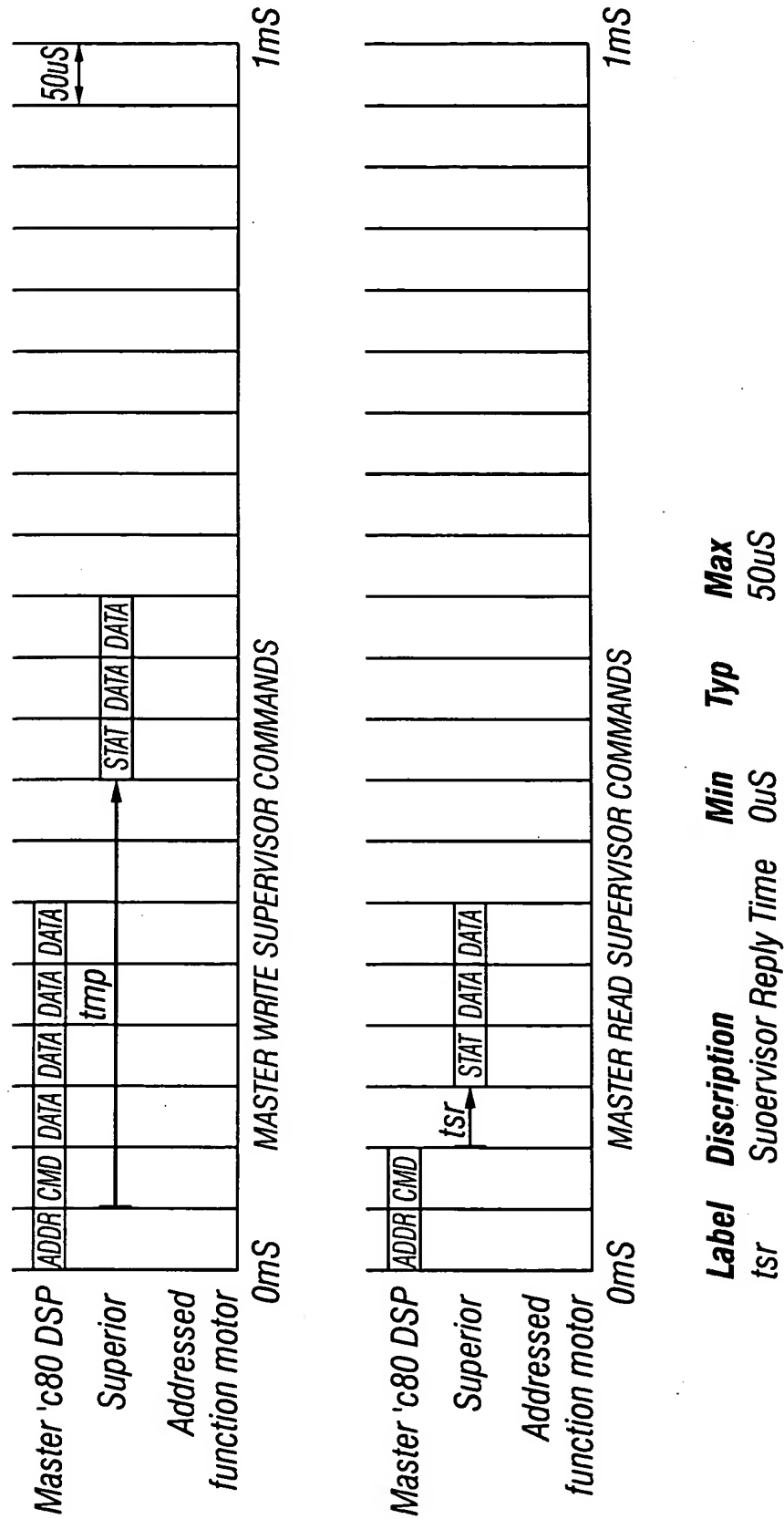


FIG. 14

Label	Description	Min	Typ	Max
tmp	Master Phase Time		350µs	
tfr	Function Reply Time	0µs		50µs
tsr	supervisor Reply Time	0µs		50µs

FIG. 15

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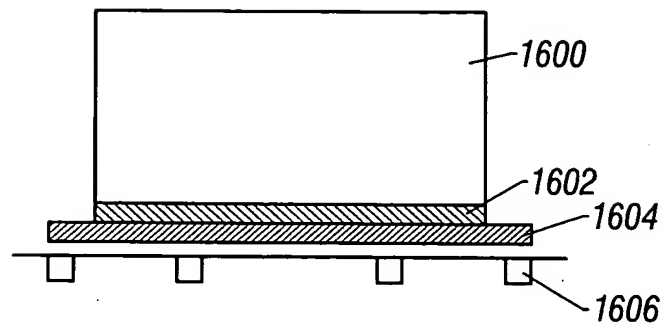


FIG. 16

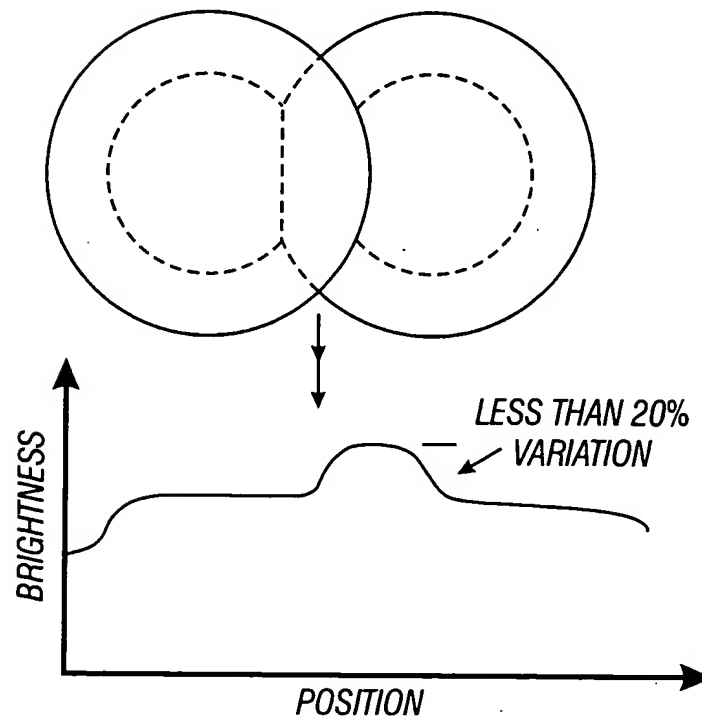


FIG. 17